

PITT STREET SEMICONDUCTOR CONFERENCE

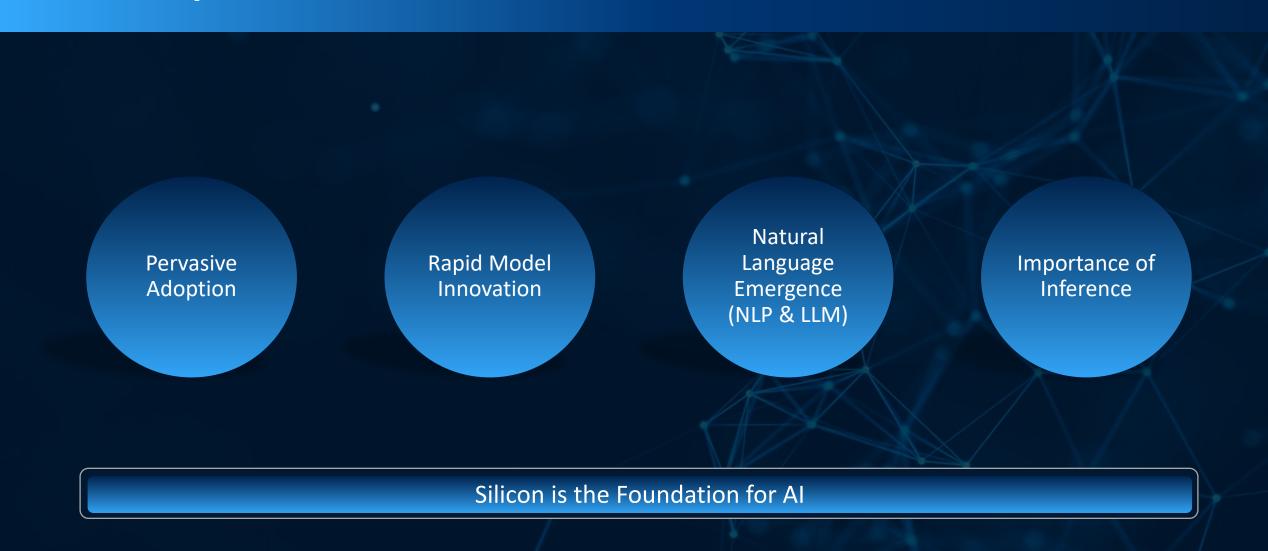
SYDNEY, MAY 2025

BrainChip Overview

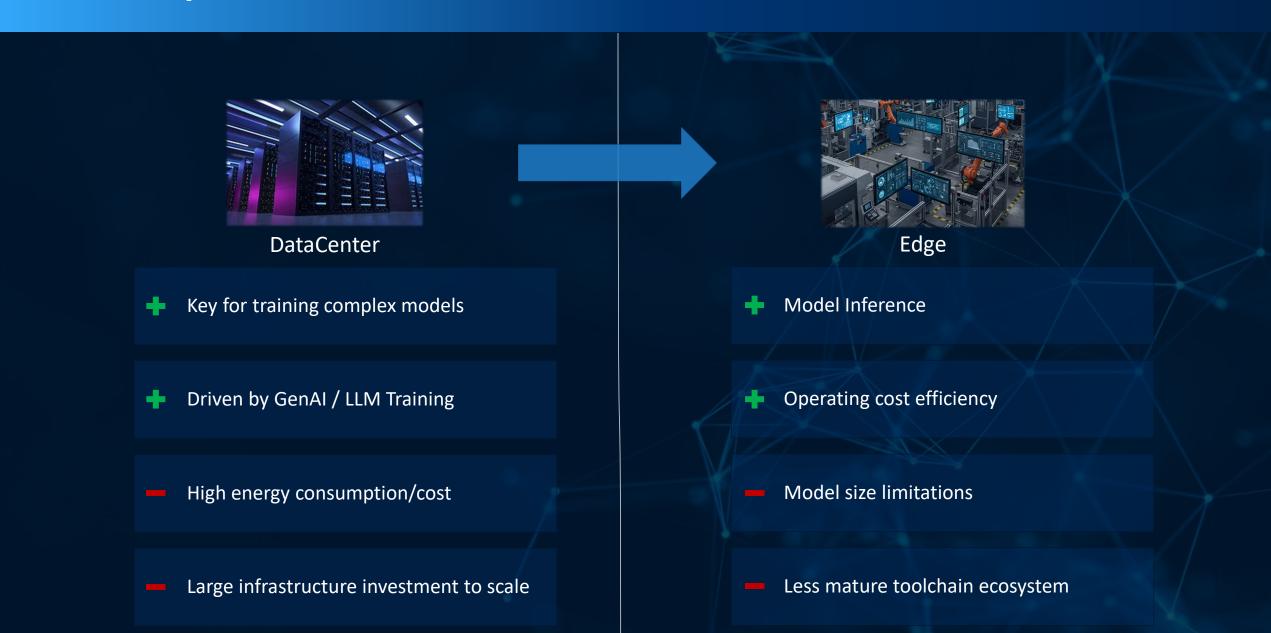




brainchip* Macro Al Trends



brainchip Shift from Cloud to the Edge



brainchip Strong Growth for both EdgeAl & Edge LLM



brainchip Edge Dedicated Accelerators

Edge AI requires specialized hardware for efficient computation Tight constraints:

- Power
- Performance
- Area



Dedicated AI chips unlock faster, more efficient, and powerful AI

brainchip* Edge Al Leader

Delivering AI IP & Silicon for ultra low-power Edge AI devices



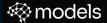
Silicon IP

- Purpose built neural processing unit
- Extremely power-efficient (mW)
- Fully scalable, programmable, re-configurable
- Digital



Software - MetaTF

Complete SW development tools and programming environment



Models













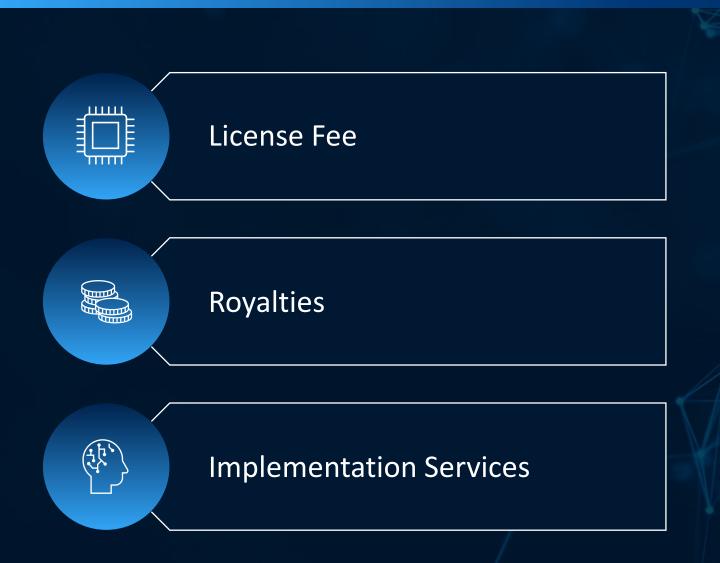


Edge VLM

brainchip* Solutions

| | Akida 1 | Akida 2 | Akida GenAl | Akida 3 |
|----------|---|---|---|--|
| Hardware | 1, 4-bit integerNeuromorphic State Machine | 1, 4, 8-bit integerNeuromorphic State Machine | 16, 32-bit Floating pointNeuromorphic (ISA) | 1, 4, 8, 16-bit integer 16, 32-bit Floating point Neuromorphic (ISA) |
| | | | <i>K</i> | |
| Software | MetaTF model conversion | Enhanced MetaTF model conversion 3rd Party Model Providers | Akida SDK for TENNs LLM Direct use of open source SSMs, no conversion necessary Distillation of very large models | Akida SDK for diverse models (TENNs and user's models) 3rd Party Model Provider ecosystem |
| | | | | |
| Models | Simple convolutional neural networks (CNNs) | CNNs, TENNs, branching and recurrent topologies | TENNs and SSM open-source Large Language Models | CNNs, TENNs, SSMs, LLMs Branching, recurrent and arbitrary topologies |
| | | | | |

brainchip Business Model



brainchip Summary



Al Shift from Cloud to Edge is under way



Dedicated optimized hardware is the enabler



Hardware market will consolidate to key leaders

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